

Appl. No. 10/034,227
Amdt. dated March 15, 2005
Reply to Office action of December 15, 2004

Amendments to the Specification:

Please replace paragraph [0001] with the following amended paragraph:

[0001] This application relates to the following commonly assigned co-pending applications entitled: ~~(fill in other applications filed same day):~~

Method And Apparatus For Efficiently Implementing Trace And/Or Logic Analysis Mechanisms On A Processor Chip, Serial No. 10/034,717, filed December 28, 2001~~(Invention Disclosure P01-3848)~~; Method And Apparatus For Managing Timestamps When Storing Data, Serial No. 10/034,482, filed December 28, 2001~~(Invention Disclosure P01-3853)~~; and Method And Apparatus For Implementing Loop Compression In A Program Counter Trace, Serial No. 10/034,506, filed December 28, 2001, now U.S. Patent No. 6,691,207, issued February 10, 2004~~(Invention Disclosure P01-3852)~~.

all of which are incorporated by reference herein.

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